

ABSTRACT OF THE DISCLOSURE

In a semiconductor integrated circuit comprising a PLL circuit including an LC resonance type voltage-controlled oscillator which oscillates at the oscillation frequency depending on the control voltage applied, a phase comparator PHC for comparing the phase of an oscillation output signal ϕ_b of said voltage-controlled oscillator with the phase of the reference clock ϕ_r , and a loop-filter LPF for outputting a voltage depending on the phase difference based on an output of the phase comparator PHC, this semiconductor integrated circuit further comprises a plurality of voltage-controlled oscillators VCO1, VCO2, which include different center frequencies of the frequency variable range and the selecting unit for selecting only one oscillator from a plurality of said voltage-controlled oscillators VCO1, VCO2, Accordingly, the semiconductor integrated circuit comprising the PLL circuit using the LC resonance type voltage-controlled oscillator having excellent phase noise characteristic and assuring the desired frequency variable range can be manufactured with higher manufacturing yield.

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